

**WHAT IS CLAIMED IS:**

1. A method for fabricating a silicon carbide semiconductor device, the method comprising the steps of:

(a) implanting impurity ions into a silicon carbide layer;

5 (b) heating the silicon carbide layer to form a carbon layer on a surface of the silicon carbide layer; and

(c) after the step (b), performing an activation annealing process with respect to the silicon carbide layer in an atmosphere at a temperature higher than in the step (b).

2. The method of claim 1, wherein

10 the step (b) includes forming the carbon layer under a pressure condition lower than an atmospheric pressure and

the step (c) includes performing the activation annealing process under a pressure condition higher than in the step (b).

3. The method of claim 1, wherein the steps (b) and (c) are performed in the same

15 heating furnace.

4. The method of claim 1, wherein the step (b) includes forming the carbon layer in the presence of a gas containing hydrogen.

5. The method of claim 1, wherein the step (b) includes forming the carbon layer under a pressure condition not lower than  $1 \times 10^{-5}$  Pa and not higher than 10 Pa.

20 6. The method of claim 1, wherein a temperature of the silicon carbide layer is not lower than 1100 °C and not higher than 1400 °C in the step (b).

7. The method of claim 1, wherein the step (c) includes performing the activation annealing process by adjusting a temperature of the silicon carbide layer to a range not lower than 1500 °C and not higher than 2000 °C under a pressure condition not lower than

25 1 kPa and not higher than 100 kPa.

8. The method of claim 1, further comprising the step of:

(d) after the step (c), heating the silicon carbide layer in the presence of a gas containing oxygen atoms to remove the carbon layer.

9. The method of claim 8, wherein a temperature of the silicon carbide layer is not 5 lower than 500 °C and not higher than 1000 °C in the step (d).

10. The method of claim 8, wherein the removal of the carbon layer in the step (d) is performed in the same heating furnace as the activation annealing process in the step (c).

11. A silicon carbide semiconductor device comprising: a silicon carbide layer; an impurity doped layer formed in a part of the silicon carbide layer; and an electrode 10 provided on the silicon carbide layer, wherein a step height at an upper surface of the silicon carbide layer is substantially the same in the impurity doped layer and in a region of the silicon carbide layer other than the impurity doped layer.

12. The silicon carbide semiconductor device of claim 11, wherein the step height at the upper surface of the silicon carbide layer is not less than 0.1 nm and not more than 15 1 nm.

13. The silicon carbide semiconductor device of claim 11, wherein a concentration of an element in the impurity doped layer other than carbon, silicon, and a dopant of the impurity doped layer is not higher than  $1 \times 10^{14} \text{ cm}^{-3}$ .

14. The silicon carbide semiconductor device of claim 13, wherein the element is 20 hydrogen, oxygen, chromium, nickel, manganese, or iron.

15. The silicon carbide semiconductor device of claim 11, further comprising:  
a gate insulating film provided on the silicon carbide layer;  
a gate electrode provided on the gate insulating film; and  
a first electrode in ohmic contact with the silicon carbide layer.

25 16. The silicon carbide semiconductor device of claim 15, further comprising:

a silicon carbide substrate provided on a lower surface of the silicon carbide layer;  
and

a second electrode in ohmic contact with a lower surface of the silicon carbide  
substrate.

5 17. The silicon carbide semiconductor device of claim 11, further comprising:

a third electrode in Schottky contact with the silicon carbide layer.